

Fig. 1(a)

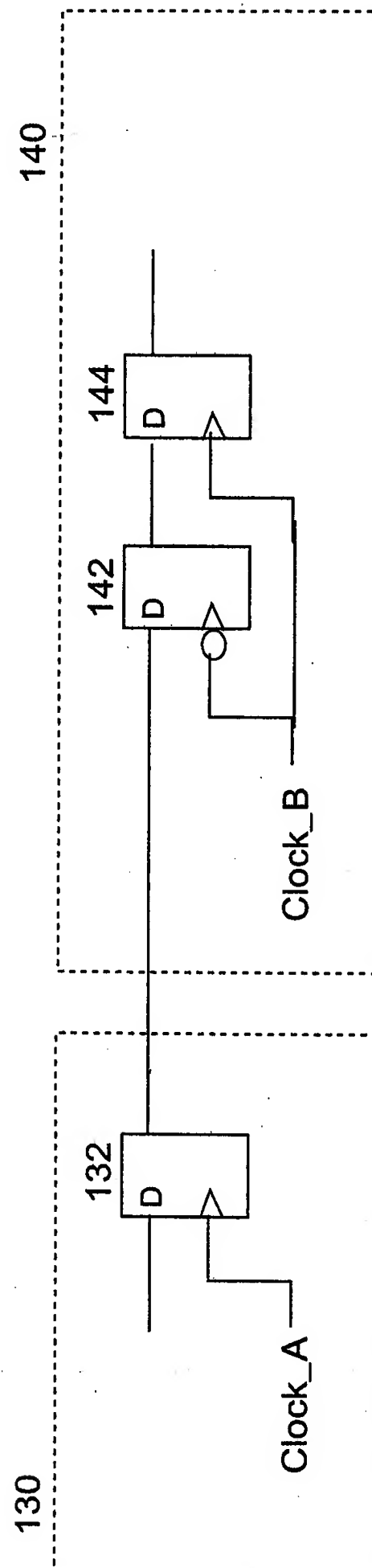


Fig. 1(b)

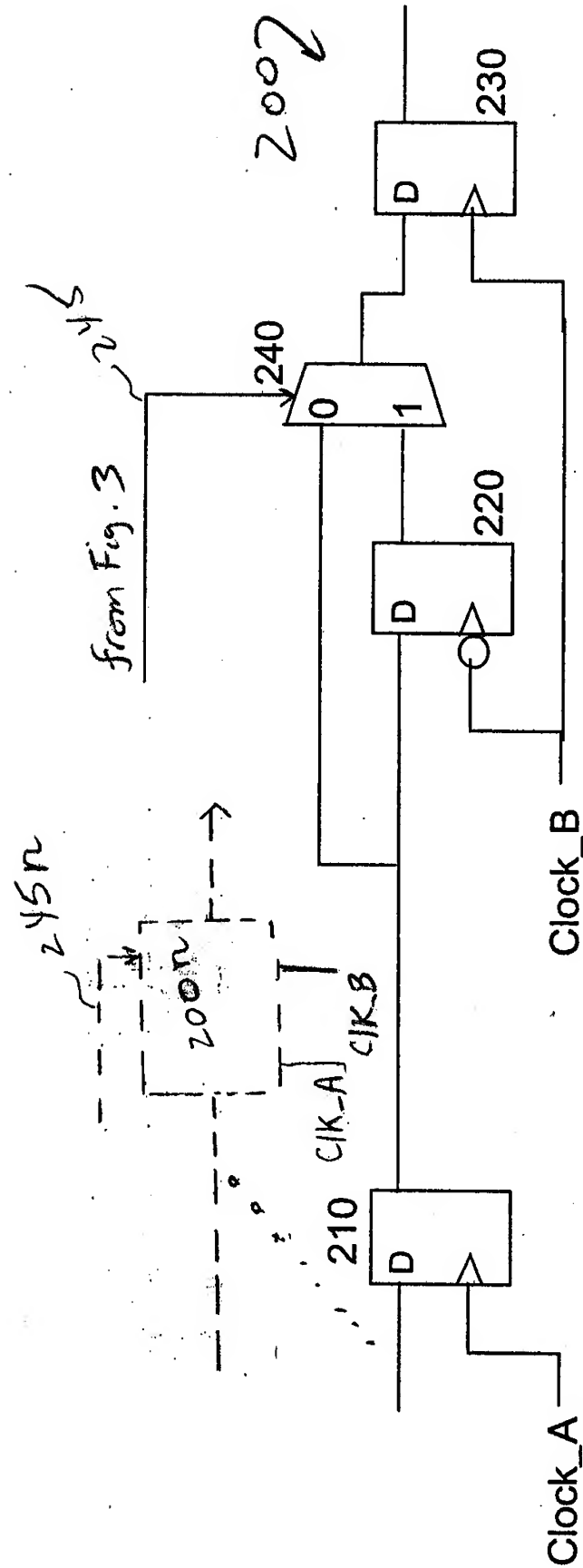


Fig. 2 (Data Path Circuit)

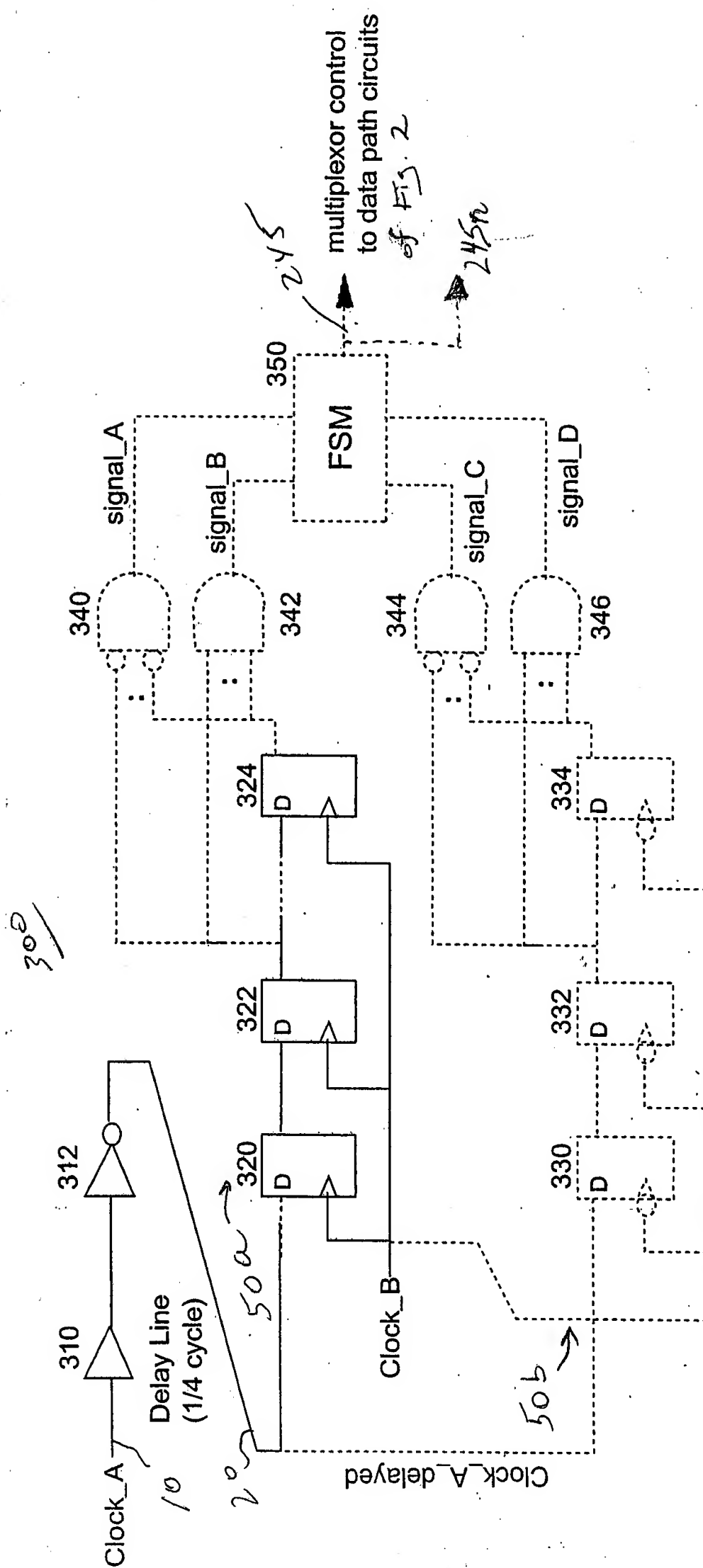


Fig. 3 (Control Circuit)

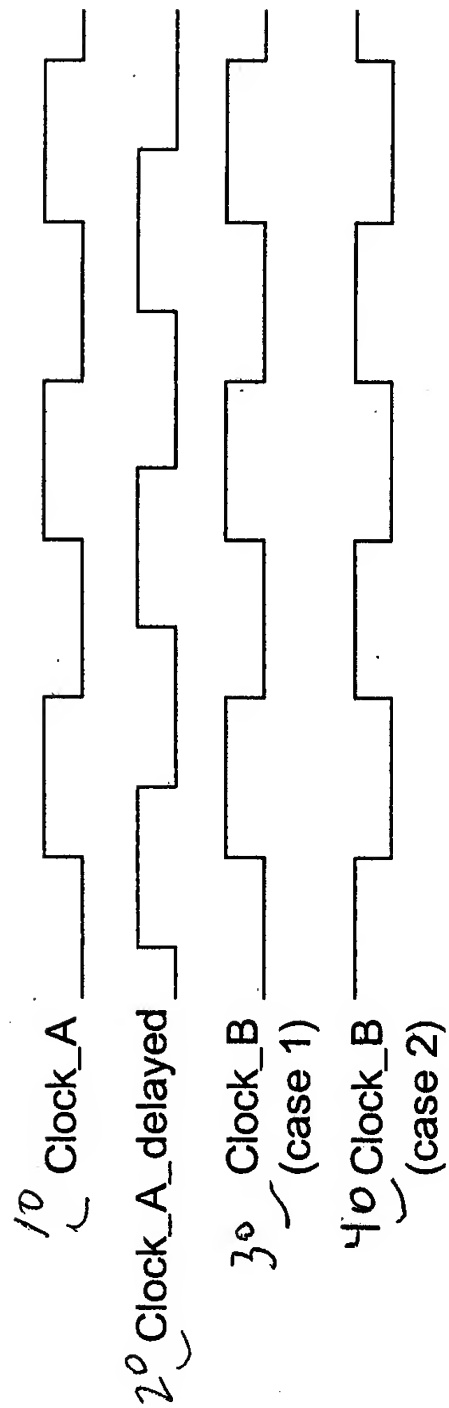


Fig. 4